

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** ( Not for submission under 37 CFR 1.99)

Application Number	10675777
Filing Date	2003-09-30
First Named Inventor	DeWitt, Jr. et al.
Art Unit	2193
Examiner Name	Chavis, John Q.
Attorney Docket Number	AUS920030477US1

## **U.S. PATENTS**

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
af	1	6973417	B1	2005-12-06	Maxwell et al.	
af	2	6721875	B1	2004-04-13	McCormick et al.	

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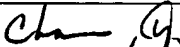
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	1	6944720	B2	2005-09-13	Sperber et al.	
	2	7035996	B2	2006-04-25	Woodall et al.	

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	1	20040030870	A1	2004-02-12	Buser	

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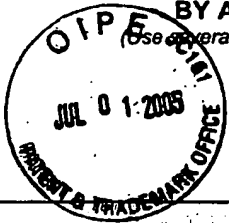
Examiner Signature	<i>Chen, Q.</i>	Date Considered	9-19-06
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			APPLICANT <b>DeWitt, Jr. et al.</b>			
			FILING DATE		GROUP ART UNIT <b>Not Assigned</b> 2193	
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
Q	AA 5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989	
	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999	
	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000	
	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999	
	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998	
	AF 5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997	
	AG 6,351,844 B1	Feb. 26, 2002	Bala	717/4	Nov. 5, 1998	
	AH 6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998	
	AI 6,442,585 B1	Aug. 27, 2002	Dean et al.	709/108	Nov. 26, 1997	
	AJ 5,774,724	Jun. 30, 1998	Heisch	395/704	Nov. 20, 1995	
	AK 5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997	
	AL 6,192,513 B1	Feb. 20, 2001	Subrahmanyam	717/5	Nov. 2, 1998	
	AM 5,691,920	Nov. 25, 1997	Levine et al.	364/551.01	Oct. 2, 1995	
	AN 6,223,338 B1	Apr. 24, 2001	Smolders	717/4	Sep. 30, 1998	
	AO 6,101,524	Aug. 8, 2000	Choi et al.	709/102	Oct. 23, 1997	
	AP 6,256,775 B1	Jul. 3, 2001	Flynn	717/4	Dec. 11, 1997	
	AQ 6,446,029 B1	Sep. 3, 2002	Davidson et al.	702/186	Jun. 30, 1999	
	AR 6,134,676	Oct. 17, 2000	VanHuben et al.	714/39	Apr. 30, 1998	
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	AX 6,206,584 B1	Mar. 27, 2001	Hastings	395/183.11	May 31, 1995	
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	AZ 6,070,009	May 30, 2000	Dean et al.	395/704	Nov. 26, 1997	
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DATE CONSIDERED 9-19-06			EXAMINER <i>Ch, G</i>			
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APPLICANT <b>DeWitt, Jr. et al.</b>			FILING DATE		GROUP ART UNIT <b>Not Assigned</b> <i>2193</i>	
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME		CLASS/ SUBCLASS	FILING DATE
<i>Q</i>	BB 2002/0129309	Sep. 12, 2002	Floyd et al.		714/724	Dec. 18, 2000
	BC 2001/0032305	Oct. 18, 2001	Barry		712/34	Feb. 23, 2001
	BD 2002/0199179	Dec. 26, 2002	Lavery et al.		717/158	Jun. 21, 2001
	BE 2002/0124237	Sep. 5, 2002	Sprunt et al.		717/127	Dec. 29, 2000
	BF 2002/0147965	Oct. 10, 2002	Swaine et al.		717/124	Feb. 1, 2001
<i>Q</i>	BG 2002/0019976	Feb. 14, 2002	Patel et al.		717/137	May 25, 2001
<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY		CLASS/ SUBCLASS	TRANSLATION YES NO
<i>Q</i>	BH JP2000029731	Dec. 8, 1999	Japan <i>Abstract only - considered</i>		G06F 9/38	X
<i>Q</i>	BI JP2000347863	Dec. 15, 2000	Japan " " "		G06F 9/38	X
<b>OTHER PRIOR ART (including author, title, date, pertinent page, etc.)</b>						
<i>Q</i>	BJ	Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.				
	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.				
	BL	Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.				
	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.				
	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.				
<i>Q</i>	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.				
<b>RELATED PATENT APPLICATIONS</b>						
EXAMINER INITIAL	APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE			FILING DATE
<i>Q</i>	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug			Nov. 4, 1999
<i>Q</i>	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System			Oct. 2, 1995
DATE CONSIDERED <i>9-19-06</i>			EXAMINER <i>Chen, Q.</i>			
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		<b>APPLICANT</b> <b>DeWitt, Jr. et al.</b>			
		<b>FILING DATE</b> <b>September 30, 2003</b>		<b>GROUP ART UNIT</b> <b>2182</b> <div style="text-align: right;">2193</div>	
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL	ATTY. DOCKET NO.	APPLICATION NO./	APPLICANT	TITLE	FILING DATE
[Signature]	AA	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
[Signature]	AB	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
[Signature]	AC	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
[Signature]	AD	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
[Signature]	AE	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
[Signature]	AF	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
[Signature]	AG	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
[Signature]	AH	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
[Signature]	AI	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
[Signature]	AJ	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
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Application Number	10/675,777
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First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Unknown <i>Chavis, J.</i>
Art Unit	2182 <i>2193</i>
Attorney Docket No.	AUS920030477US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
<i>GC</i>	AA 4,034,353	07-05-1977	Denny et al.	
	AB 4,821,178	04-11-1989	Levin et al.	
	AC 5,151,981	09-29-1992	Westcott et al.	
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	AE 5,594,864	01-14-1997	Trauben	
	AF 5,691,920	11-25-1997	Levine et al.	
	AG 5,758,168	05-26-1998	Mealey et al.	
	AH 6,480,966 B1	11-12-2002	Rawson, III	
	AI 2003/0135720 A1	07-17-2003	DeWitt et al.	
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	AK 6,185,652 B1	02-06-2001	Shek et al.	
	AL 4,145,735	03-20-1979	Soga	
	AM 5,548,762	08-20-1996	Creedon et al.	
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	AR 6,775,728 B2	08-10-2004	Zimmer et al.	
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	AT 6,275,893 B1	08-14-2001	Bonola	
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<i>GC</i>	AW Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 <sup>nd</sup> Ed., 1982, pp. 434-443.
<i>GC</i>	AX Inoue, "Digital mobile communication system designed for nationwide police activities - WIDE system", IEEEExplore, October 1996, pp. 1-2.

Examiner: *Chavis, J.*Date Considered: *9-19-06*

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Date Considered: 9-19-06

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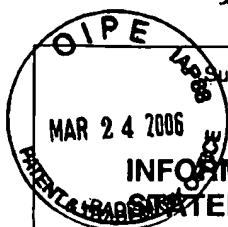
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	AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

Examiner: Chao, G.

Date Considered: 9-19-06

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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5,710,881	01-20-1998	Gupta et al.	
AB	5,822,763	10-13-1998	Baylor et al.	
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AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.
AI	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

Examiner:

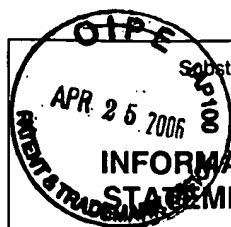
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Attorney Docket No.	AUS920030477US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5,797,019	08-18-1998	Levine et al.	
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AF	6,925,424 B2	08-02-2005	Jones et al.	

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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## **NON-PATENT LITERATURE**

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>			
AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2			
AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>			

Examiner:

Chen, J.

Date Considered:

9-19-06

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Substitute for form 1449/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

### Complete if Known

Application Number	10/675,777
Filing Date	09/30/2003
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Chavis, John Q.
Art Unit	2193
Attorney Docket No.	AUS920030477US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5051944	09-24-1991	Fetterolf et al.	
AB	5142634	08-25-1992	Fite et al.	
AC	5394529	02-28-1995	Brown, III et al.	
AD	6233679 B1	05-15-2001	Holmberg	
AE	5887159	03-23-1999	Burrows	
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AL	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

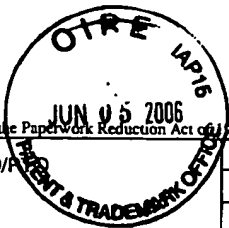
Examiner:

*Chavis, J.*

Date Considered:

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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5805879	09-08-1998	Hervin et al.	
AB	6647301 B2	11-11-2003	Sederlund et al.	
AC	6735666 B1	05-11-2004	Koning	
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Examiner:

*Chavis, J.*

Date Considered:

7-17-06

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STATEMENT BY APPLICANT**

JUN 19 2006

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Filing Date	09/30/2003
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Examiner Name	Chavis, John Q.
Art Unit	2193
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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	6,149,318	11-21-2000	Chase et al.	
AB	6,185,671 B1	02-06-2001	Pentovski et al.	
AC	6,247,113 B1	06-12-2001	Jaggar	
AD	6,460,135 B1	10-01-2002	Suganuma	
AE	2003/0126590 A1	07-03-2003	Burrows et al.	
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AG	MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support Runtime Optimization", IEEE, 1999, pp. 136-147.

Examiner:

Chavis, J.

Date Considered:

9-19-06

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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5,857,097	01-05-1999	Henzinger et al.	
AB	6,820,155	11-16-2004	Ito	
AC	5,745,770	04-28-1998	Thangadurai et al.	

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